HackerPort schematic proposition
version 0.2 date 2004-04-29
see http[s]://1stein.schuermans.info/hackerport/
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dotted sections are optional

+13V
JTAG
PICPROG
CON4
USB LED
D1
R5
74HC573
IC3
VCC
GND
D0
D1
D2
D3
D4
D5
D6
D7
100n
C9
DEBUG
CON1
R1
R2
ANALOG INPUTS
CON5
PWM OUTPUTS
CON6
I2C BUS
CON7
C1
C2
C3
C4
X1
40MHz
10p
PIC18F442
IC1
USBN9604
IC2
24MHz
15p
C7
C8
R3
1M
R4
1K5
C5
C6
1u
USB-B
CON2
MAX232
IC4
C10
C11
C12
C13
C14
UART
CON8
OUTPUTS A
CON9
C15
INPUTS A
CON11
OUTPUTS B
CON10
C16
INPUTS B
CON12
C17
C18
R6
R7
R8
R9
R10
R11
R12
R13
T1
T2
T3
1K
10K
100