http[s]://1stein.schuermans.info/hackerport/

hacker port schematic
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dotted sections are optional

13V GENERATOR
D4 D3 D2 D1
10uF C23 10uF C22 10uF C21 10uF C20 10uF C19
T5 1KΩ R15
T4 1KΩ R14
+13V
VCC GND
D0 D1 D2 D3 D4 D5 D6 D7
JTAG
GND 2 1 TCK
VCC 4 3 TDO
6 5 TMS
8 7 !TRST
GND 10 9 TDI
CON3
100nF C9
74HC573 IC3
VCC OE
Q0 D0
Q1 D1
Q2 D2
Q3 D3
Q4 D4
Q5 D5
Q6 D6
Q7 D7
LE GND
T3 1KΩ R9 10KΩ R8
T2 1KΩ R7
100Ω R11
100Ω R10
T1 1KΩ R6
PICPROG
1 VPP
2 VCC
3 GND
4 PGD
5 PGC
CON4
USB LED
LED1 1KΩ R5
100Ω R12
100Ω R13
DEBUG
CON1 1 VPP 2 VCC 3 GND 4 PGD 5 PGC
10KΩ R1
10KΩ R2
USBN9604 IC2
CS RD SK/WR INTR DRQ DACK A0/ALE/SI D0/SO D1 D2 D3 D4 D5 D6
CLKOUT XOUT XIN MODE0 MODE1 GND VCC GND D- D+ V3.3 AGND RESET D7
15pF C7
1MΩ R3 24MHz X2
15pF C8
100nF C5
USB-B
VCC D- D+ GND GND GND
CON2
1.5KΩ R4
1uF C6
ANALOG INPUTS
1 VCC
2
3
4
5
6
7 GND
CON5
100nF C1
10pF C3
10pF C4
40MHz X1
PIC18F442 IC1
VPP/MCLR RB7/PGD
RA0/AN0 RB6/PGC
RA1/AN1 RB5/PGM
RA2/AN2 RB4
RA3/AN3 RB3/CCP2
RA4/T0CKI RB2/INT2
RA5/AN4 RB1/INT1
RE0/AN5/RD RB0/INT0
RE1/AN6/WR VCC
RE2/AN7/CS GND
VCC RD7/PSP7
GND RD6/PSP6
OSC1/CLKI RD5/PSP5
OSC2/CLKO RD4/PSP4
RC0/T1OSO/T1CKI RC7/RX/DT
RC1/T1OSI/CCP2 RC6/TX/CK
RC2/CCP1 RC5/SDO
RC3/SCK/SCL RC4/SDI/SDA
RD0/PSP0 RD3/PSP3
RD1/PSP1 RD2/PSP2
100nF C2
1uF C10
MAX232 IC4
VCC GND T1OUT R1IN R1OUT T1IN T2IN R2OUT
C1+ VS+ C1- C2+ C2- VS- T2OUT R2IN
1uF C11
1uF C13
1uF C12
1uF C14
UART
TX 1
RX 2
GND 3
CON8
PWM OUTPUTS
1 VCC
2
3
4 GND
CON6
I2C BUS
1 VCC
2 SCL
3 SDA
4 GND
CON7
OUTPUTS A
1 VCC
2
3
4
5
6
7
8
9
10 GND
CON9
74HC573 IC5
VCC OE
Q0 D0
Q1 D1
Q2 D2
Q3 D3
Q4 D4
Q5 D5
Q6 D6
Q7 D7
LE GND
100nF C15
INPUTS A
VCC 1
2
3
4
5
6
7
8
9
GND 10
CON11
74HC573 IC7
VCC OE
Q0 D0
Q1 D1
Q2 D2
Q3 D3
Q4 D4
Q5 D5
Q6 D6
Q7 D7
LE GND
100nF C17
OUTPUTS B
1 VCC
2
3
4
5
6
7
8
9
10 GND
CON10
74HC573 IC6
VCC OE
Q0 D0
Q1 D1
Q2 D2
Q3 D3
Q4 D4
Q5 D5
Q6 D6
Q7 D7
LE GND
100nF C16
INPUTS B
VCC 1
2
3
4
5
6
7
8
9
GND 10
CON12
74HC573 IC8
VCC OE
Q0 D0
Q1 D1
Q2 D2
Q3 D3
Q4 D4
Q5 D5
Q6 D6
Q7 D7
LE GND
100nF C18